Appln. No.: 10/632,051

Amendment Dated May 20, 2010

Reply to Office Action of January 27, 2010

Amendments to the Specification:

Please replace paragraph [0046] with the following rewritten paragraph:

The message serial interface on the RF section 202 operates as a slave device to the baseband section 204 (or other master device that adheres to the characteristics set forth below). The inputs bits to the RF section 202 (on the MSG_DI line) are shifted into a 32 bit shift register 227 in the RF section 202 under control of the MSG_CLK. In one implementation, up to 32 bits are sent in one message block and data is received and transmitted with the most significant bit first. Simultaneously, the MSG_DO output bits are shifted out of the other end of the same shift register 227. If output from the RF section 202 is not needed, then the MSG_DO output need not be connected. In one implementation, the MSG_CLK operates at up to 20 MHz and the message serial interface signals are, approximately, above 0.8*VCC V for logic 1 and below 0.2*VCC V for logic 0.

Please replace paragraph [0063] with the following rewritten paragraph:

Bits 2-7 in the message format shown in Table 4 are power control bits. Those bits control whether particular hardware elements in the RF section 202 are powered-up or powered-down. The bits may be transferred from the shift register 227 in the RF section 202 and applied to power control circuits that apply or remove power from specific hardware elements. For example, bit 5, the PLL_Pwr bit, controls power for a phase locked loop (PLL) circuit and frequency divider in the RF section 202. When the RF section 202 receives a message that has bit 5 cleared, the RF section 202 may remove power from the PLL and divider circuitry by opening a switch through which power flows, driving a power regulator control pin, or through another mechanism. Similarly, when the RF section 202 receives a message that has bit 5 set, the RF section 202 may apply power to the PLL and divider circuitry by closing the switch, enabling the power regulator, or the like.